

Figure 1

200

Block Address 0	Data for cache line "A" (512 bytes)				ECC
Block Address 1	Data for cache line "B" (512 bytes)				ECC
Block Address 2	Data for cache line "C" (512 bytes)				ECC
Block Address 3	Data for cache line "D" (512 bytes)				ECC
⋮	⋮				⋮
Block Address n	Data for cache line "x" (512 bytes)				ECC
Block Address n+1	Metadata Cache line "A"	Metadata Cache line "B"	Metadata Cache line "C"	Metadata Cache line "D"	ECC
Block Address n+2	Metadata Cache line "D+1"	Metadata Cache line "D+2"	Metadata Cache line "D+3"	Metadata Cache line "D+4"	ECC
⋮	⋮				⋮
Block Address n+m	Metadata Cache line "x-3"	Metadata Cache line "x-2"	Metadata Cache line "x-1"	Metadata Cache line "x"	ECC

Figure 2

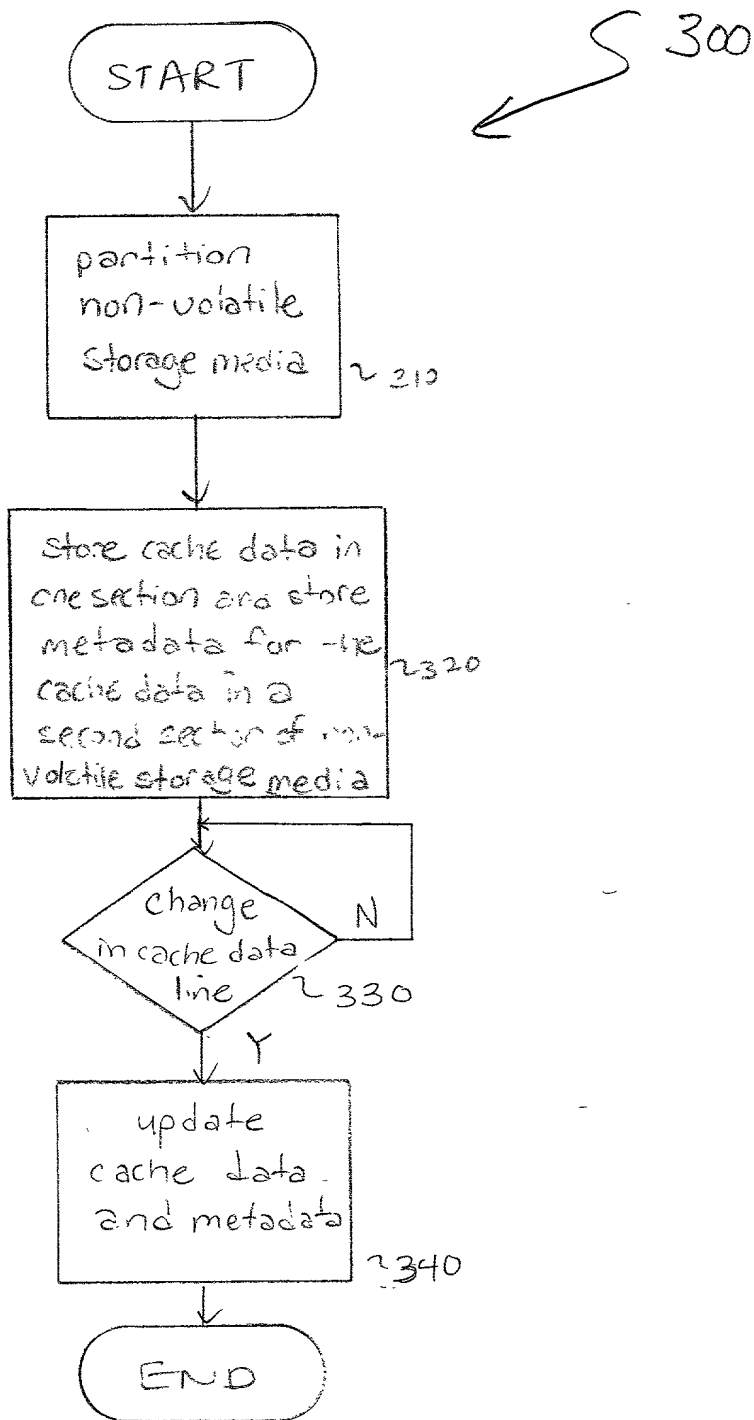


Figure 3